



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## In re Application of:

Whitman et al.

Serial No.: 09/542,783

**Filed:** April 4, 2000

For: SPIN COATING FOR MAXIMUM FILL CHARACTERISTIC YIELDING A PLANARIZED THIN FILM SURFACE

Confirmation No.: 6870

Examiner: B. Kebede

**Group Art Unit: 2823** 

Attorney Docket No.: 2269-4294US

(98-1208.00/US)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313

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## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document listed on attached Form PTO-1449 or PTO/SB/08 be considered by the Examiner and made of record. A copy of the listed document is enclosed pursuant to 37 C.F.R. § 1.98(a).

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In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

## Other Documents

VAN ZANT, Peter, "Microchip Fabrication, A Practical Guide to Semiconductor Processing," pp. 176-178 and 185-187.

Applicants offer to supply any explanation or discussion of the document which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed after the mailing date of the final Office Action under 37 C.F.R. § 1.113, but before payment of the issue fee.

I hereby certify that no item of information contained in the Supplemental Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of the statement. Pursuant to 37 C.F.R. § 1.97(d)(ii), applicant hereby requests consideration of the accompanying Supplemental Information Disclosure Statement. The fee

pursuant to 37 C.F.R. §1.17(p) for consideration of this Supplemental Information Disclosure Statement is enclosed.

Respectfully submitted,

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Date: July 29, 2003

BGP/dlm:djp

Enclosures: Form PTO-1449 or PTO/SB/08

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Substitute for form 1449A/PTO			Complete if Known
INDODMATION	ICCI OCUDE	Application Number	09/542,783
INFORMATION D		Filing Date	April 4, 2000
STATEMENT BY APPLICANT		First Named Inventor	Whitman et al.
		Group Art Unit	2823
(use as many sheets as necessary)		Examiner Name	B. Kebede
Sheet 1 of	1	Attorney Docket Number	2269-4294US (98-1208.00/US)

OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS					
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>		
		VAN ZANT, Peter, "Microchip Fabrication, A Practical Guide to Semiconductor Processing," pp. 176-178 and 185-187.			
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Examiner	Date	
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<sup>&</sup>lt;sup>1</sup> Unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.